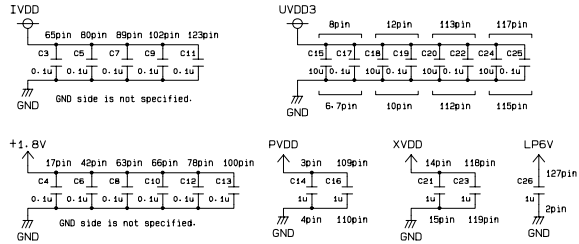


CN4 and CN5 Setting :
* Connect 1-2 when supplying Power to CPU board
* Connect 2-3 or Open when No supplying Power to CPU board

JP5 Setting :
* Connect 1-2 when supplying +3.3V to CVDD
* Connect 1-3 when supplying +1.8V to CVDD
(Solder Connection)

Note: Close the capacitor to the IC's specified pin.



担当	検図	確認	承認	S1720V05 GPP EVA BOARD
oshita				USB2.0
				1/1
				2005/12/19

セイコーエプソン 株式会社