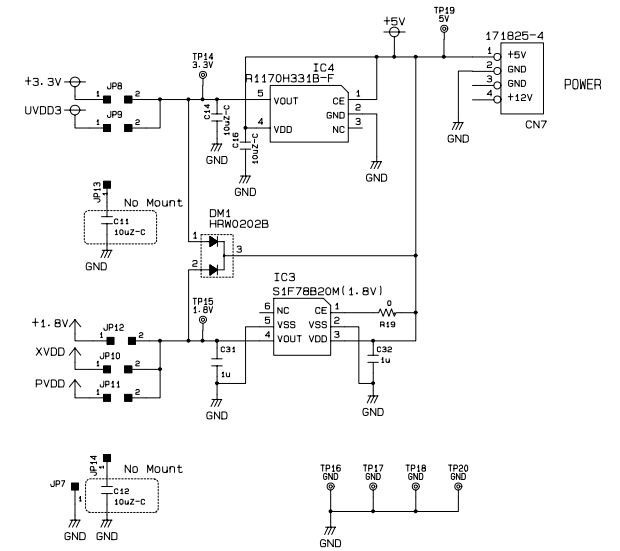


CN2 and CN3 Setting :
* Connect 1-2 when supplying Power to CPU board
* Connect 2-3 or Open when No supplying Power to CPU board

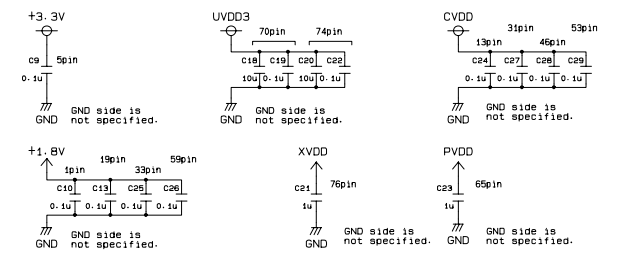
JP3 Setting :
* Connect 1-2 when CPU I/F level is +3.3V.
* Connect 1-3 when CPU I/F level is +1.8V.

CR1 or CRU1 is available alternatively.
When CR1 is used, remove CRU1 and mount R18.
When CRU1 is used, remove CR1, R18 and connect XI-pin to GND, open XO-pin.
You must set the S1R72V17's register properly about the clock source and frequency.

JP17 and R21:
See MAX8586 Document sheet.



Note: Close the capacitor to the IC's specified pin.



When you use CN5 as USB-Device and CN6 as USB-Host:
JP15 = OPEN
JP16 = Connect 1-2
JP18 = Connect

When you use only CN5 as USB-Device/Host :
JP15 = Connect
JP16 = Connect 1-3
JP18 = OPEN
R9-R10 = No Mount is recommended.

担当	検図	確認	承認	S1R72V17 QFP EVA BOARD R0H5
satake				USB2.0 1/1 2006/8/8
セイコーエプソン 株式会社				