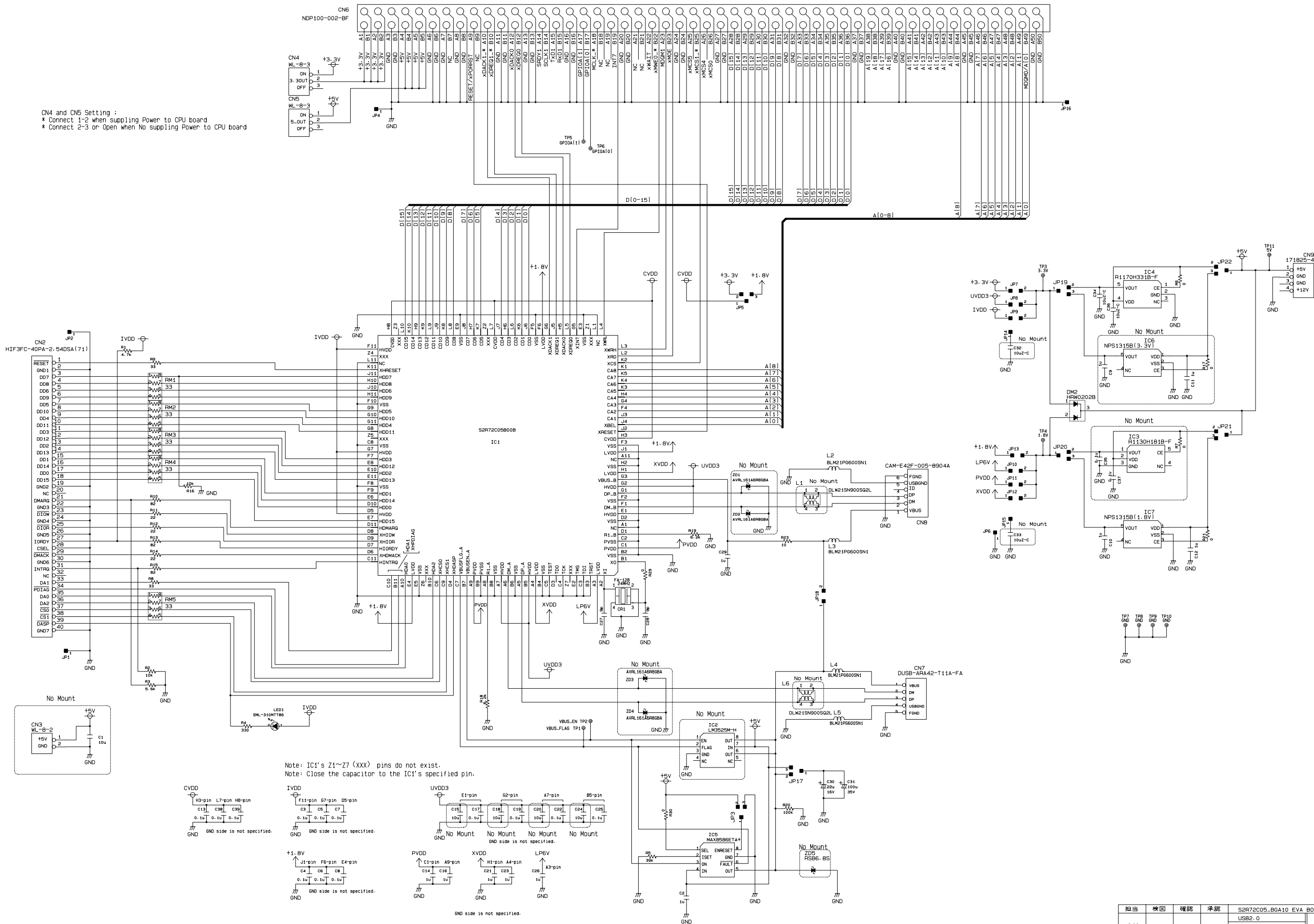
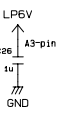
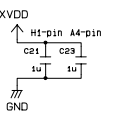
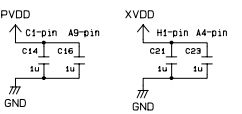
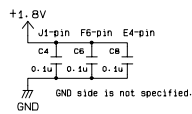
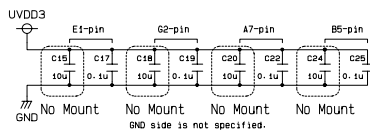
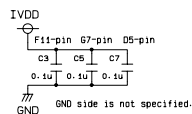
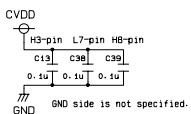


CN4 and CN5 Setting :  
\* Connect 1-2 when supplying Power to CPU board  
\* Connect 2-3 or Open when No supplying Power to CPU board



Note: IC1's Z1~Z7 (XXX) pins do not exist.  
Note: Close the capacitor to the IC1's specified pin.



担当	検図	確認	承認	S2R72C05-BGA10 EVA BOARD
oshita				USB2.0
				1/1
				2006/5/24

セイコーエプソン 株式会社