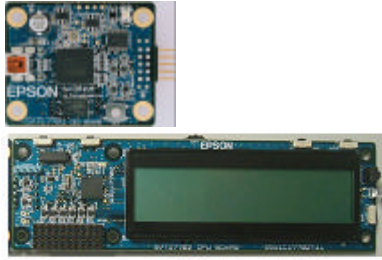


S5U1C17702T1100 (SVT17702)

Software eValuation Tool for S1C17702



- Evaluation board of S1C17702
- STN LCD panel (32comx72seg B/W)
- Infrared transmitter LED/Receiver module
- Expansion interface connectors (GPIO, UART, I2C and SPI)
- Key input circuit (4 keys)
- OSC3 external clock input
- I with an ICD board (can evaluate without S5U1C17001H)
- A lot of sample programs (refer to User 's site)

■ DESCRIPTIONS

S5U1C17702T1100 (Software evaluation Tool for S1C17801, Hereafter referred to as SVT17702) is an evaluation board for S1C17702 manufactured by SEIKO EPSON.

The SVT17702 consists of two circuit boards: CPU and LCD boards. By connecting these boards, the SVT17702 allows the user to debug the application software without an ICD or other debugging tools.

In addition, it provides extended interfaces such as serial ports, allowing you to connect your own expansion boards to the SVT17702.

■ FEATURES

CPU Board

CPU	S1C17702
Input power voltage	+3.3 VDC (supplied through the ICD interface or CR2032 coin cell battery)
CPU input clock	OSC1: 32.768 kHz OSC3: 6MHz
On-board functions/devices	- STN LCD panel (display size: 32 commons x 128 segments, black & white) (32 commons and 72 segments can be driven) - Reset switch - Expansion interface connectors (GPIO, UART, I2C and SPI) - ICD board connector - Key input circuit (4 keys) - Rotary encoder with switch - Infrared transmitter LED/Receiver module

ICD Board

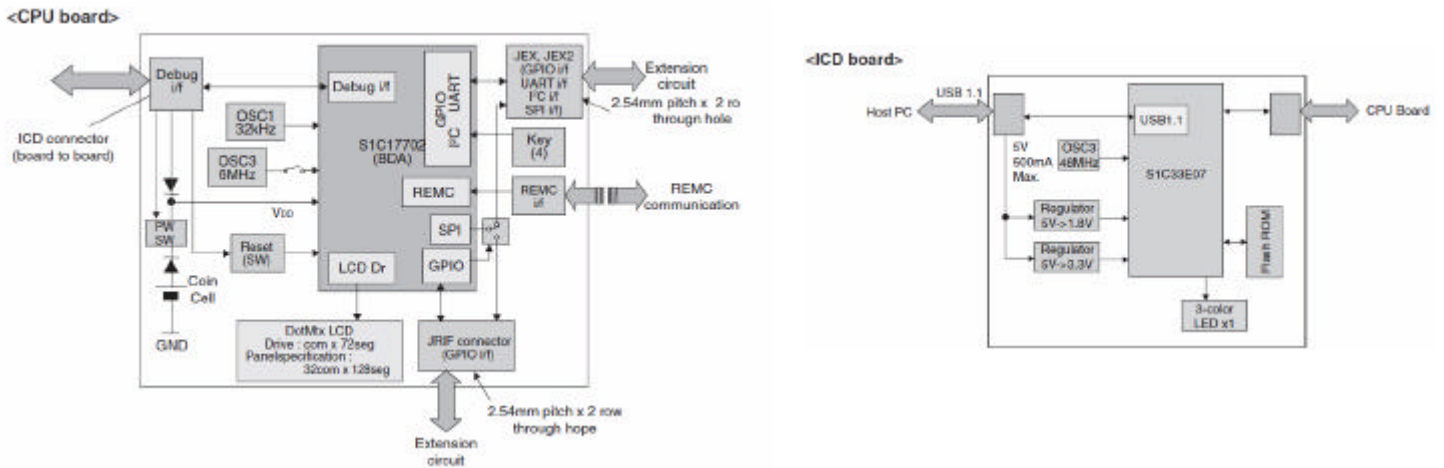
PC interface	USB 1.1
Power supply voltage	USB bus power (On-board regulator output voltage of 3.3 V)
On-board functions/devices	- Tri-color status LED - Reset switch - CPU board connector

■ Users site list of sample programs

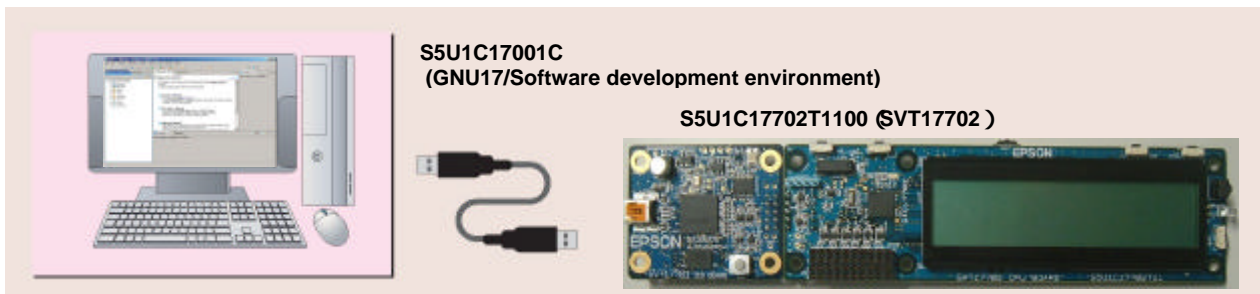
Oscillation switching OSC1/IOSC (HSCLK)/OSC3(HSCLK)	SPI master
GPIO	SPI slave
16 bit timer	I ² C
8 bit timer	IrDA transfer and reception
PWM/capture timer	LCD Dr
8 bit OSC1 timer	SVD
Clock timer	Watchdog timer
Stopwatch timer	Sleep/Halt instruction example
UART	Remote controller (plan)
Current consumption measurement example	

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■ Block Diagram



■ Connection Diagram



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