

Epson S1D13781 Renesas SH7720 Connection Guide

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For information on the Renesas SH7720, visit their website at:

https://www.renesas.com/en-us/doc/products/mpumcu/doc/superh/r01uh0083ej_sh7720_hm.pdf?key=5b0df3cfdae63050d77b0ee410da85c5

For detailed information, refer to the following sections:

9.5.2 Normal Space Interface (1) Basic Timing

9.5.3 Access Wait Control

9.5.4 CSn Assert Period Expansion

For information on the Epson S1D13781, visit our website at:

https://global.epson.com/products_and_drivers/semicon/information/document_download/display_controllers.html

For detailed information, refer to the following sections:

4.4 Host Interface Pin Mapping

9.4.2 Direct 16-bit Mode 2 Timing

SH3 SH7720	Direction	S1D13781	
		S1D13781 Pin Name	Direct 16-bit Mode2
	-	CNF[2:0]	001
CS#	→	CS#	CS#
RD#	→	WR#	RDU#
RD#	→	RD#	RDL#
WE1#	→	UB#	WRU#
WE0#	→	LB#	WRL#
(see S1D13781 spec for usage)		AB0	TE
AB1	→	AB1	AB1
AB[18:2]	→	AB[18:2]	AB[18:2]
DB0	↔	DB0	DB0
DB1	↔	DB1	DB1
DB[7:2]	↔	DB[7:2]	DB[7:2]
DB8	↔	DB8	DB8
DB[15:9]	↔	DB[15:9]	DB[15:9]

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